

Spray Deposited Nanostructured CuO Thin Films: Influence of Substrate Temperature and Annealing Process

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In this study, CuO thin films were deposited on glass substrates at a wide range of temperatures from 450°C to 550°C with steps of 25°C by chemical spray pyrolysis technique. Aiming to investigate the effect of annealing process, one of the resulting films was annealed at 450°C for 3 hours under ambient air. Based on X-ray diffraction, all the resulting films are monoclinic with two prominent peaks at ~36° and ~39°. The crystallite size of the CuO film deposited at 450°C was found to be the largest in comparison with the others. As the substrate temperature increased, a gradual change was observed for the CuO thin film surface morphology and in the case of annealed film, the grains and their boundaries became indistinguishable. The resistivity of the films was reduced by virtue of increasing the substrate temperature and also, both the mobility and carrier concentration of the annealed film were improved drastically after annealing. As expected, the CuO thin films absorption was considerable in the visible region and gradually declined after 800nm. The estimated band gap value of the CuO film deposited at 450°C were fairly close to the optimum band gap for solar applications.

Keywords: *CuO thin film, spray pyrolysis technique, substrate temperature, annealing process.*

1. Introduction

Solar cell technology is one of the promising technologies, which has been the main focus of attention in the last decades and yet it demands more study to make it more cost-effective and therefore competitive with the conventional sources of energy. Among the active layers which have thus far been used in solar cells, cupric oxide (CuO) has recently received a lot of critical attention. Apart from a rather high theoretical conversion efficiency¹, abundance and nontoxicity make CuO thin film a wise choice for solar applications².

CuO with its monoclinic structure naturally exhibits *p*-type conductivity and the band gap values of 1.3-2.1 eV has been reported in literatures³. The possibility of depositing CuO with almost all conventional methods, such as sputtering⁴, sol-gel⁵, chemical vapor deposition⁶, thermal deposition⁷, can provide the groundwork for the deposition of CuO films with a wide variety of characteristics. However, the main drawback of the most sophisticated methods is the fact that they require highly expensive devices and processes. In comparison, chemical spray pyrolysis is a non-vacuum and solution-based technique along with suitability for mass production, which makes it a more convenient and less expensive method for thin film deposition⁸.

One can deposit almost all the oxide semiconductors by adjusting the spray deposition parameters such as precursor properties⁹, substrate temperature¹⁰, flow rate¹¹, nozzle-substrate distance¹², etc. Among the spray deposition parameters, the substrate temperature and annealing temperature are the most

common parameters optimized to obtain a thin film with desirable features. In the last decade, there has been several studies in which the influence of aforementioned factors has been investigated^{13,14}; yet in this study, CuO thin films have been deposited in high substrate temperatures starting from 450°C and finishing at 550°C in which the deposition rate declines drastically. It was found that, up to 525°C, as the substrate temperature increases, the resistivity of the resulting CuO thin films decreases and again it increases for the film deposited at 550°C. Furthermore, with the purpose of studying the influence of annealing process, one of the deposited films was annealed. Finally, the structural, morphological, electrical, and optical properties of the resulting films have been studied.

2. Experimental

Nanostructured CuO thin films were deposited on well-cleaned glass substrates via spray pyrolysis. Precursors were prepared by dissolving appropriate amount of Copper (II) chloride hydrate in deionized water to obtain a starting solution with 0.05M molarity. The resulting solution was sprayed by a homemade spray apparatus with a custom class gun having a nozzle diameter of 0.2mm. In order to deposit each film, 0.9bar filtered air was applied to the nozzle positioned at 29cm above the substrate with the substrate temperature varying from 450°C to 550°C with steps of 25°C. The optimized experimental conditions of spray deposition have been given in Table 1. Finally, with the purpose of studying the influence of the annealing process, the film deposited at 500°C was also annealed at 450°C for 3 hours.

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Table 1. Optimized experimental parameters of the spray deposition.

Parameters	Values
Precursor	Copper(II) chloride dihydrate (0.05M) in deionized water
Temperature	450°C, 475°C, 500°C, 525°C, 550°C
Precursor volume	30ml
Nozzle-substrate distance	29cm
Carrier gas pressure	0.9bar
Spray angle	90°
Spray nozzle diameter	0.2mm

The structure of the abovementioned deposited CuO thin films were studied by Grazing Incident X-ray diffraction (GIXRD) (X'Pert PRO MPD model) with Cu-K α radiation. A surface profilometer (Bruker Dektak XT1) was applied to measure as-prepared steps on the surface of the CuO films. The surface morphology was examined by a field emission scanning electron microscopy (FESEM) (JEOL JSM-7610F) and atomic force microscopy (AFM) (FemtoScan SPM). The Hall Effect measurements (Phys Tech) were performed on 7×7 mm² CuO thin films using 0.56T magnetic field in room temperature. The Van der Pauw contacts (coplanar) geometry was employed for all measurements. Electrical contacts were made to each of the four corners with silver paste. Optical transmittance, absorbance and reflectance of the resulting films were recorded by means of an UV-vis spectrometer (Perkin-Elmer Lambda 25).

3. Results and Discussion

3.1. Structural properties

Fig. 1 shows the grazing incident X-ray diffraction (GIXRD) of the CuO thin films prepared at different substrate temperatures via spray pyrolysis technique. All the deposited films are polycrystalline with a monoclinic crystal structure, and there is no trace of the other common phase of copper oxide (Cu₂O). There are two prominent peaks at ~36° and ~39° which can be well indexed to the monoclinic CuO (JCPDS Card No. 045-0937)¹⁵⁻¹⁷.

In order to study the influence of substrate temperature on the structural properties of the deposited films, the mean crystallite size (*D*) was estimated for two preferential orientations of the resulting films using the Scherrer's equation:

$$D = \frac{0.94\lambda}{\beta \cos \theta} \quad (1)$$

where λ is the wavelength of the X-rays, β is the full width at half-maximum (FWHM) of diffraction peaks (in radians), and θ is the Bragg's diffraction angle¹⁸. The results have been

listed in Table 2, and the variation of the mean crystallite size for the two major peaks along with the intensities of the aforementioned peaks has been shown in Fig. 2.

One can see that the largest crystallite size up at the substrate temperature of 450°C and gradually decreases as the substrate temperature increases. However, there is a slight increase in the crystallite size of the film deposited at the substrate temperature of 550°C compared with 525°C which might be attributed to the lower rate of deposition in a high temperature, resulting in a larger crystallite size. Annealing the as-deposited layer at 500°C led to an increase in the crystallite size; yet, it was still lower than the mean crystallite size of the as-deposited film at the substrate temperature of 450°C.

Furthermore, the lattice parameters ($a \neq b \neq c$, $\alpha = \gamma = 90^\circ \neq \beta$) and unit cell volume (*V*) of CuO thin films have been calculated from the following equations:

$$\frac{1}{d^2} = \frac{1}{\sin^2 \beta} \left(\frac{h^2}{a^2} + \frac{k^2 \sin^2 \beta}{b^2} + \frac{l^2}{c^2} - \frac{2hl \cos \beta}{ac} \right) \quad (2)$$

$$V = abc \sin \beta \quad (3)$$

where *a*, *b*, *c* and β are the lattice parameters for the monoclinic structure, (*h k l*) are the Miller indices and *d* is the interplanar distance¹⁹. The results have been shown in Table 3.

3.2. Morphological properties

The surface micrographs of the CuO thin films deposited at 450°C, 500°C, and 550°C have been shown in Fig. 3a-c, respectively. The surface morphology of these films exhibits a random grain distribution on the surface with mainly trapezium grain shapes. The estimated mean grain sizes for the CuO films have been shown in Table 4. As can be seen, the mean grain size increases from 138nm for

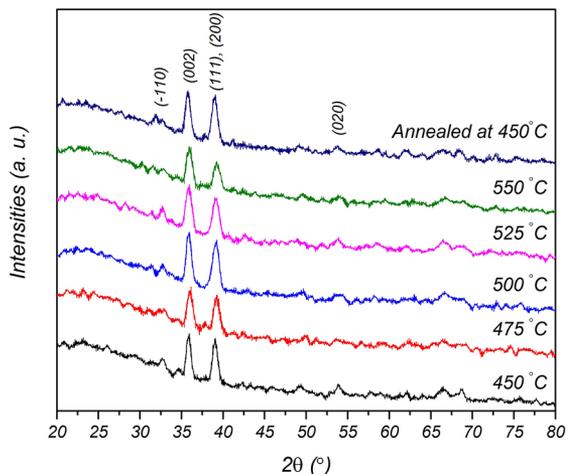
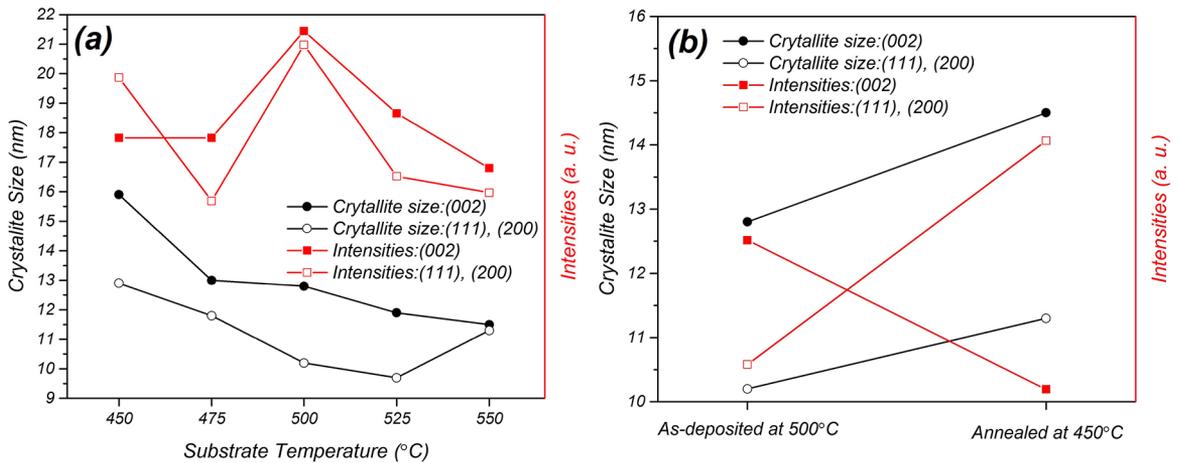


Figure 1. GIXRD pattern of the as-deposited CuO thin films at different substrate temperatures and the CuO thin film deposited at 500°C after annealing at 450°C for 3 hours in air.

Table 2. Thickness and calculated crystallite size of the CuO thin films deposited at different substrate temperatures and the CuO thin film annealed at 450°C for 3 hours in air for two prominent peaks.

Substrate Temperature (°C)	Thickness (nm)	2θ (°)	d (Å)	Crystallite Size (nm)
450	230	35.899	2.498	15.9
		39.077	2.306	12.9
475	233	36.058	2.485	13.0
		39.274	2.295	11.8
500	242	35.909	2.498	12.8
		39.123	2.294	10.2
525	221	35.899	2.499	11.9
		39.151	2.295	9.7
550	99	36.012	2.493	11.5
		39.262	2.298	11.3
Annealed at 450	221	35.811	2.515	14.5
		39.012	2.305	11.3

**Figure 2.** The variation of crystallite size and peak intensity of (a) the CuO thin film deposited at different temperature, (b) the CuO film deposited at 500°C before and after annealing process.**Table 3.** Lattice parameters (a, b, c, β) and unite cell volume (V) of the CuO thin films deposited at different substrate temperatures and the CuO thin film annealed at 450°C.

Substrate Temperature (°C)	a (Å)	b (Å)	c (Å)	β (°)	V (Å) ³
450	4.640	3.400	5.060	99.120	78.823
475	4.615	3.400	5.039	99.535	77.978
500	4.681	3.399	5.084	100.678	79.497
525	4.644	3.401	5.078	100.233	78.937
550	4.633	3.396	5.056	99.508	78.464
Annealed at 450	5.061	3.410	5.205	104.903	86.811

the film deposited at 450°C to 283nm for the one deposited at 550°C, and the later film has a more dense and regular surface morphology.

The FESEM micrograph of the film which has been annealed at 450°C for 3 hours is shown in Fig. 3d. As can be seen, the surface morphology of the annealed CuO film

is distinctly different from those of the other films in which the heat treatment has transferred the trapezium-shaped grains to irregular grains in a way that the grains and their boundaries cannot be clearly distinguished.

Fig. 4a-d show 3D AFM images of the thin films deposited at 450°C, 500°C, and 550°C, and annealed at

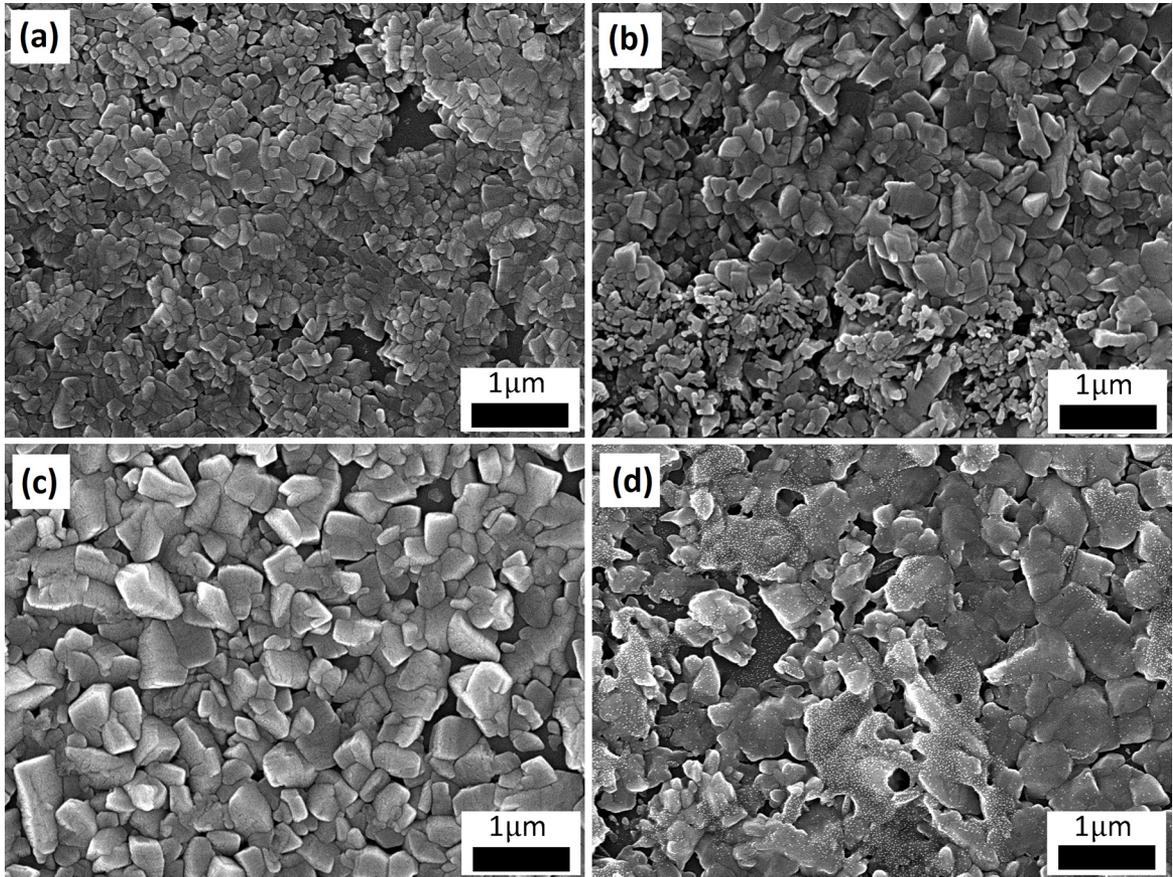


Figure 3. FESEM micrograph of the CuO thin films deposited at (a) 450°C (b) 500°C (c) 550°C (d) 500°C and annealed at 450°C for 3 hours in air.

Table 4. Morphological characteristics of the CuO thin films deposited at 450°C, 500°C, 550°C and the CuO thin film annealed at 450°C.

Substrate Temperature (°C)	Average Surface Roughness (nm)	Root Mean Square Roughness (nm)	Skewness	Kurtosis	Mean Grain Size (nm)
450	24.84	33.17	-0.185	1.687	138
500	13.21	16.88	0.047	0.265	199
550	24.83	31.87	-0.128	0.533	283
Annealed at 450	21.61	28.15	-0.165	0.851	-

450°C, respectively. Also, their corresponding roughness parameters have been listed in Table 4. In our study, as the substrate temperature increases, the mean surface roughness and mean square roughness first decrease and then, increase to the extent that both the thin films deposited at the substrate temperature of 450°C and 550°C have almost same mean surface roughness. Although the skewness is negative in the both cases, which implies the dominance of the peaks over the valleys, the kurtosis is way smaller (almost one third) at 550°C than at 450°C, indicating that the peaks are less sharp and the valleys are less deep in this case²⁰. Finally, the annealed film, compared with its as-deposited equivalence, shows a rougher surface with a negative skewness and larger kurtosis.

3.3. Electrical properties

The Hall Effect measurement system with the van der Pauw configuration was applied to measure the electrical properties of the CuO thin films deposited at different temperatures by spray pyrolysis, and the results are listed in Table 5. The variation of the mobility is quite similar to the mean crystallite size trend which we believe shows the close correlation between crystallinity and mobility in the resulting films. This tendency in mobility might be attributed to the reduction in barrier height at the grain boundaries by virtue of a larger crystallite size at lower substrate temperatures^{19,21}.

It is well known that the *p*-type conductivity in CuO thin films are the direct result of Cu vacancies in lattice structure which leads to the formation of holes in valence bond²².

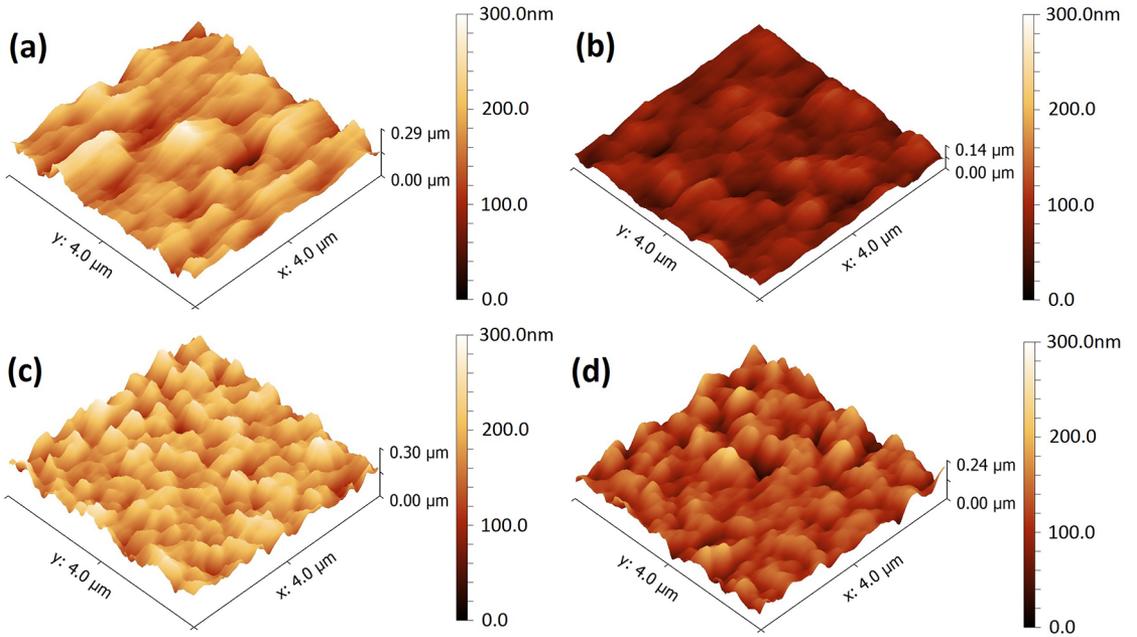


Figure 4. 3D AFM images of the CuO thin films deposited at (a) 450°C (b) 500°C (c) 550°C (d) 500°C and annealed at 450°C for 3 hours in air.

Table 5. Electrical properties and bandgap values of the CuO thin films deposited at different substrate temperatures and the CuO thin film annealed at 450°C.

Substrate Temperature (°C)	Mobility ($\text{cm}^2\text{V}^{-1}\text{s}^{-1}$)	Carrier Concentration (cm^{-3})	Resistivity (Ωcm)	Band Gap (eV)
450	9.79	$8.03 \times 10^{+13}$	7.94×10^3	1.6415 ± 0.0002
475	4.64	$4.29 \times 10^{+14}$	3.14×10^3	1.747 ± 0.001
500	1.71	$2.10 \times 10^{+15}$	1.73×10^3	1.750 ± 0.001
525	0.698	$8.77 \times 10^{+15}$	1.01×10^3	1.782 ± 0.001
550	1.96	$1.90 \times 10^{+15}$	1.68×10^3	1.837 ± 0.002
Annealed at 450	2.97	$2.85 \times 10^{+16}$	7.37×10^1	1.719 ± 0.001

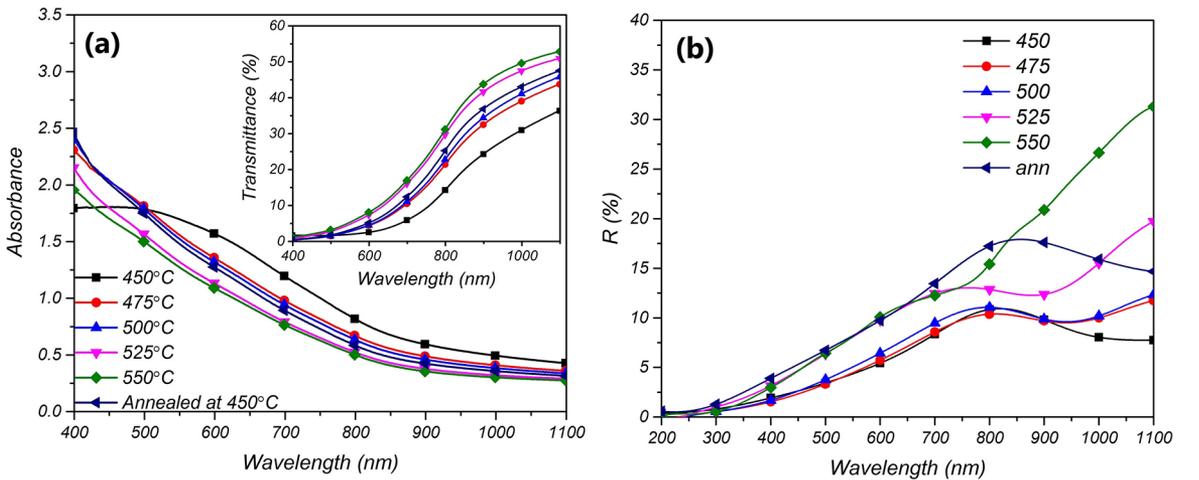


Figure 5. (a) Optical absorbance (b) reflectance spectra of the CuO thin films deposited at different substrate temperatures and the CuO thin film annealed at 450°C. Inset shows the transmittance spectra of the resulting films.

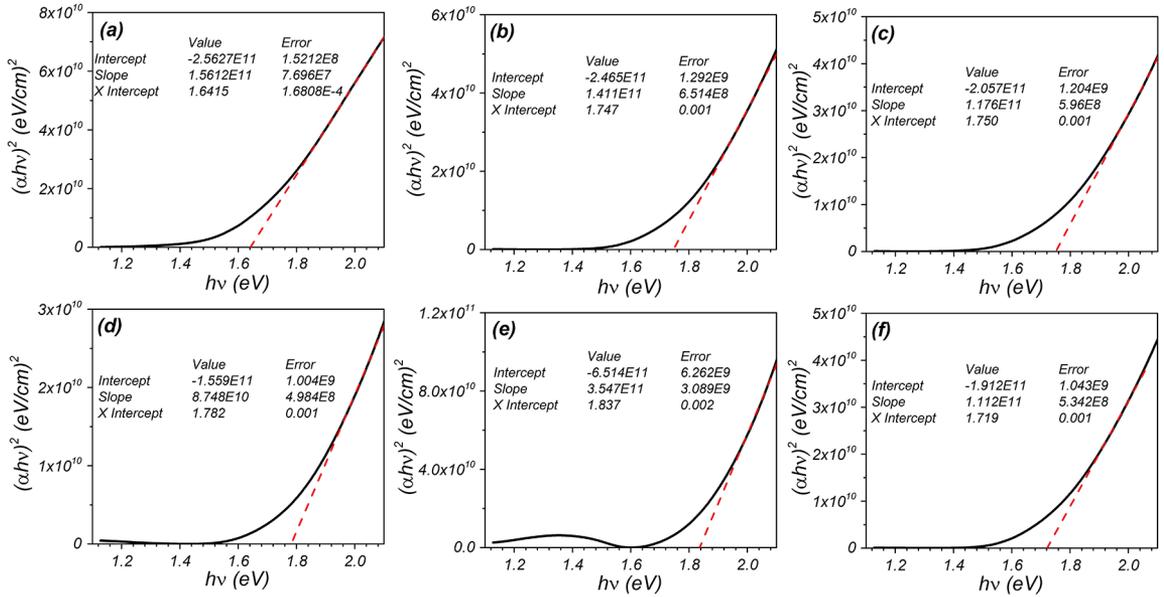


Figure 6. Plots of $(\alpha h\nu)^2$ vs. $h\nu$ for the resulting thin films deposited at (a) 450°C (b) 475°C (c) 500°C (d) 525°C (e) 550°C (f) 500°C and annealed at 450°C for 3 hours in air.

Therefore, based on the carrier concentration values listed in Table 5, it can be concluded that, as a result of increasing the substrate temperature, the number of Cu vacancies increases, resulting in a higher hole carrier concentration. However, one can see an increase in the mobility and reduction in the carrier concentration at 550°C which might be due to the lower rate of deposition at this temperature which can lead to a better crystallinity and reduce the number of defects in the lattice of the deposited film¹⁵.

As for the annealed film, one can see the significant influence of the annealing process on the resulting film. Both the mobility and carrier concentration in the annealed film meet a considerable raise in comparison with the as-deposited one, leading to a distinctive conductivity^{23,24}. On the one hand, as a result of the annealing process, not only does the crystallite size increase, but the surface morphology also changes, which seems to be in favor of a better mobility²¹. However, it causes a better oxidation and therefore, a higher concentration of oxygen in nonstoichiometric CuO which increases the hole-carrier concentration in the annealed film².

3.4. Optical properties

Fig. 5 shows the optical absorbance and reflectance spectra of the thin films deposited at different temperatures along with the annealed one. As expected from the CuO thin films, the resulting films exhibit a strong absorption in the visible region; however, it declines after 800nm to the extent that the transparency is considerable in the range of 900nm to 1100nm. These are the basic characteristics of a proper solar selective absorber²⁵.

In order to estimate the bandgap of the CuO thin films, first, the transmittance (T) and reflectance (R) experimental

data were converted to the absorption coefficient (α) via the following equation:

$$\alpha = \frac{1}{t} \ln \left[\frac{(1-R)^2}{2T} + \sqrt{\frac{(1-R)^4}{4T^2} + R^2} \right] \quad (4)$$

where t is thickness^{3,26}. The optical bandgap (E_g) and absorption coefficient are directly related via the Tauc relation:

$$\alpha h\nu = A(h\nu - E_g)^n \quad (5)$$

where $h\nu$ is the photon energy, A is an energy independent constant, and n is $\frac{1}{2}$ for direct allowed transition. Therefore, the bandgap can be estimated by plotting $(\alpha h\nu)^2$ versus $h\nu$ and extrapolating the linear part of the Tauc plot to $(\alpha h\nu)^2=0$ ²⁷. The Tauc plot of the resulting CuO thin films have been shown in Fig. 6, and the estimated values of the band gap have been listed in Table 5. The resulting band gap values are in good agreement with the literature^{13,28}.

It seems that, among the factors which can possibly alter the band gap, the crystallite size of the CuO films are mainly responsible for the variation of the bandgap in this study. The correlation between crystallite size and band gap has been discussed earlier in literature^{14,29}. Accordingly, by decreasing the crystallite size with increasing the substrate temperature, the band gap generally increased. Finally, owing to a larger crystallite size, the band gap value of the annealed film was reduced, as compared with the as-deposited one.

4. Conclusion

In the present work, the influence of the substrate temperature and annealing on the some physical properties

of the CuO thin films, were reported. The crystallite size is maximum at 450°C and gradually declines while the substrate temperature increases. Based on morphological studies, the surface morphology of CuO thin films changes to a more regular grain shape with a significant increase in their size by virtue of increasing the substrate temperature. The hall effect measurement reveals that increasing the substrate temperature improves the conductivity of the CuO films by increasing the carrier concentration, while leading to a reduction in the mobility. It seems that the variation of the crystallite size is mainly responsible for the bandgap variation. The results have determined that the annealing process has a tremendous impact on the physical properties of the deposited film at 500°C, as it leads to a film with a better crystallinity, totally different surface morphology, considerably improved conductivity, and a redshift in the band gap.

5. Acknowledgements

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6. References

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